

X86 Printable Opcode Table

OpcodeChar	Instruction	OpcodeChar	Instruction	OpcodeChar	Instruction
20	AND [m8], r8	40	@ INC AX/EAX ^{*3}	60	` PUSHAW/PUSHAD ^{*4}
21	! AND [m16/32], r16/32 ^{*1}	41	A INC CX/ECX ^{*3}	61	a POPAW/POPAD ^{*4}
22	" AND r8, [m8]	42	B INC DX/EDX ^{*3}	62	b BOUND ...
23	# AND r16/32, [m16/32] ^{*1}	43	C INC BX/EBX ^{*3}	63	c ARPL ...
24	\$ AND AL, i8	44	D INC SP/ESP ^{*3}	64	d FS: PREFIX
25	% AND AX/EAX, i16/32 ^{*2}	45	E INC BP/EBP ^{*3}	65	e GS: PREFIX
26	& ES: PREFIX	46	F INC SI/ESI ^{*3}	66	f OPERAND SIZE OVERRIDE
27	' DAA	47	G INC DI/EDI ^{*3}	67	g ADDRESS SIZE OVERRIDE
28	(SUB [m8], r8	48	H DEC AX/EAX ^{*3}	68	h PUSH i32 ^{*5}
29) SUB [m16/32], r16/32 ^{*1}	49	I DEC CX/ECX ^{*3}	66 68	fh PUSH i16 ^{*5}
2A	* SUB r8, [m8]	4A	J DEC DX/EDX ^{*3}	69	i IMUL r32, [m32], i32
2B	+ SUB r16/32, [m16/32] ^{*1}	4B	K DEC BX/EBX ^{*3}	66 69	fi IMUL r16, [m16], i16 (i16 not i32)
2C	, SUB AL, i8	4C	L DEC SP/ESP ^{*3}	6A	j PUSH i8
2D	- SUB AX/EAX, i16/32 ^{*2}	4D	M DEC BP/EBP ^{*3}	6B	k IMUL r32, [m32], i8
2E	. CS: PREFIX	4E	N DEC SI/ESI ^{*3}	66 6B	fk IMUL r16, [m16], i8
2F	/ DAS	4F	O DEC DI/EDI ^{*3}	6C	l INSB
30	0 XOR [m8], r8	50	P PUSH AX/EAX ^{*3}	6D	m INSW/INSD ^{*6}
31	1 XOR [m16/32], r16/32 ^{*1}	51	Q PUSH CX/ECX ^{*3}	6E	n OUTSB
32	2 XOR r8, [m8]	52	R PUSH DX/EDX ^{*3}	6F	o OUTSW/OUTSD ^{*6}
33	3 XOR r16/32, [m16/32] ^{*1}	53	S PUSH BX/EBX ^{*3}	70	p JO o8
34	4 XOR AL, i8	54	T PUSH SP/ESP ^{*3}	71	q JNO o8
35	5 XOR AX/EAX, i16/32 ^{*2}	55	U PUSH BP/EBP ^{*3}	72	r JB o8
36	6 SS: PREFIX	56	V PUSH SI/ESI ^{*3}	73	s JAE o8
37	7 AAA	57	W PUSH DI/EDI ^{*3}	74	t JE o8
38	8 CMP [m8], r8	58	X POP AX/EAX ^{*3}	75	u JNE o8
39	9 CMP [m16/32], r16/32 ^{*1}	59	Y POP CX/ECX ^{*3}	76	v JBE o8
3A	: CMP r8, [m8]	5A	Z POP DX/EDX ^{*3}	77	w JA o8
3B	; CMP r16/32, [m16/32] ^{*1}	5B	[POP BX/EBX ^{*3}	78	x JS o8
3C	< CMP AL, i8	5C	\ POP SP/ESP ^{*3}	79	y JNS o8
3D	= CMP AX/EAX, i16/32 ^{*2}	5D] POP BP/EBP ^{*3}	7A	z JP o8
3E	> DS: PREFIX	5E	^ POP SI/ESI ^{*3}	7B	{ JPO o8
3F	? AAS	5F	_ POP DI/EDI ^{*3}	7C	JL o8
				7D	} JGE o8
				7E	~ JLE o8